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Applicant No. : 09/752,869 Confirmation No. 1463
Applicants : Akhilesh KUMAR
Filed : December 29, 2000
For : METHOD AND SYSTEM FOR COMPLETING PURGE REQUESTS IN A
MULTI-NODE MULTIPROCESSOR SYSTEM
Group Art Unit : 2111
Examiner : Raymond Ngan PHAN

PAPER ENTITLED: PTO Transmittal Form (PTO/SB/21)
AMENDMENT

1 page
9 pages

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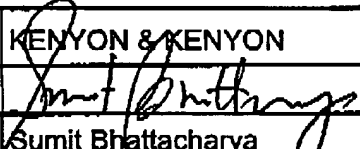
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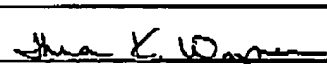
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/752,869	
	Filing Date	December 29, 2000	
	First Named Inventor	Akhilesh KUMAR	
	Art Unit	2111	
	Examiner Name	Raymond Ngan PHAN	
Total Number of Pages in This Submission	10	Attorney Docket Number	2207/9861

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Printed name	Sumit Bhattacharya	
Date	May 23, 2005	Reg. No. 51,469

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DOCKET NO.: Intel 2207/9861
ASSIGNEE: Intel Corporation**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Thea K. Wagner**AMENDMENT**

Sir:

The following remarks are respectfully submitted in response to the office action dated February 23, 2005.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks/Arguments begin on page 7 of this paper.

SJ01 70390 v1